

Revision History

Revision	Date	Comments
1306	2013-06-09	初次版本
1412	2014-12-22	1.增加加密芯片 2.改为两片内存 3.改进RESET电路 4.使用eMMC 5.0

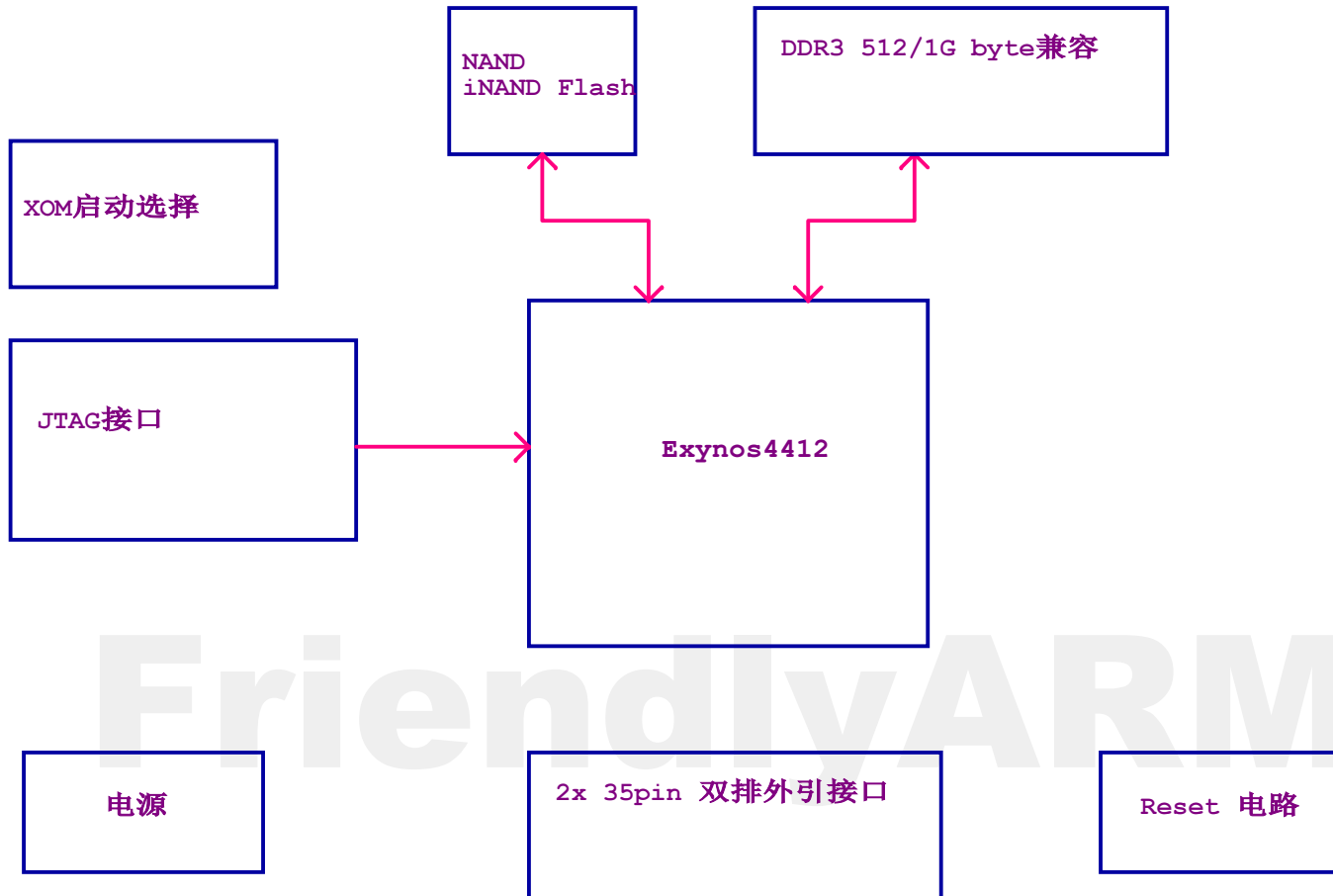
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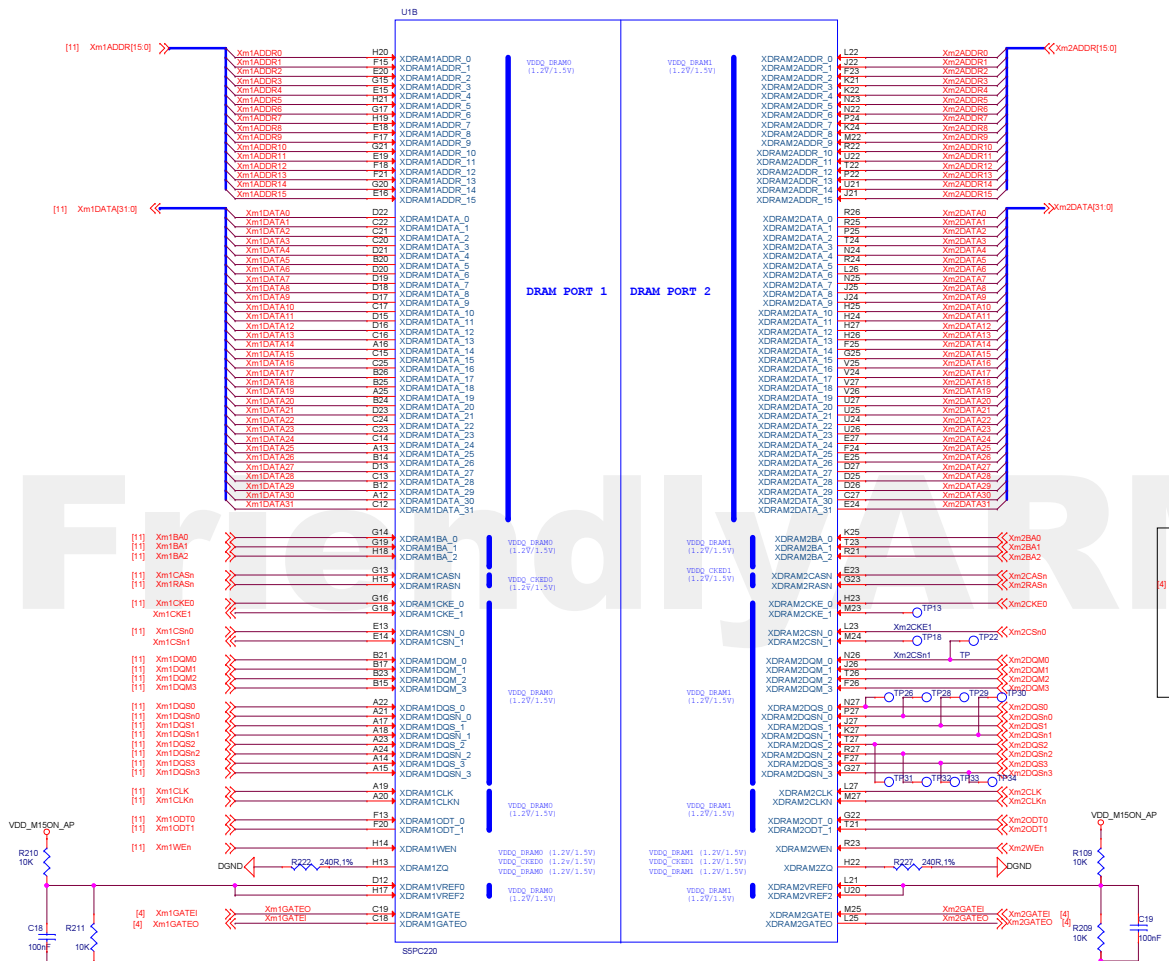
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[4] Xn1GATE1 Xn1GATE2 Xn1GATE3 Xn1GATE4 [4]

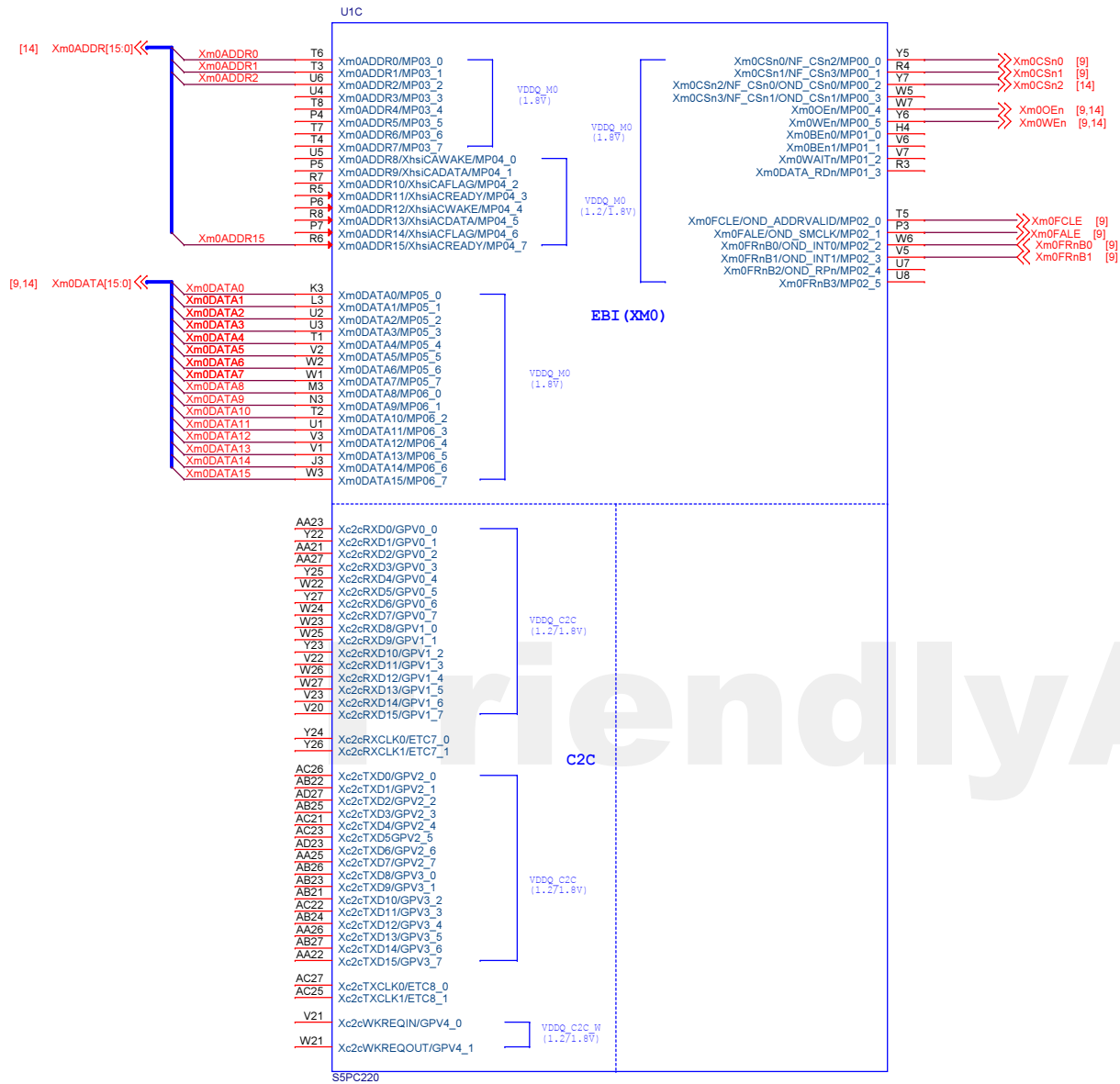
线与clk等长

[4] Xn2GATE1 Xn2GATE2 Xn2GATE3 Xn2GATE4 [4]

线与clk等长

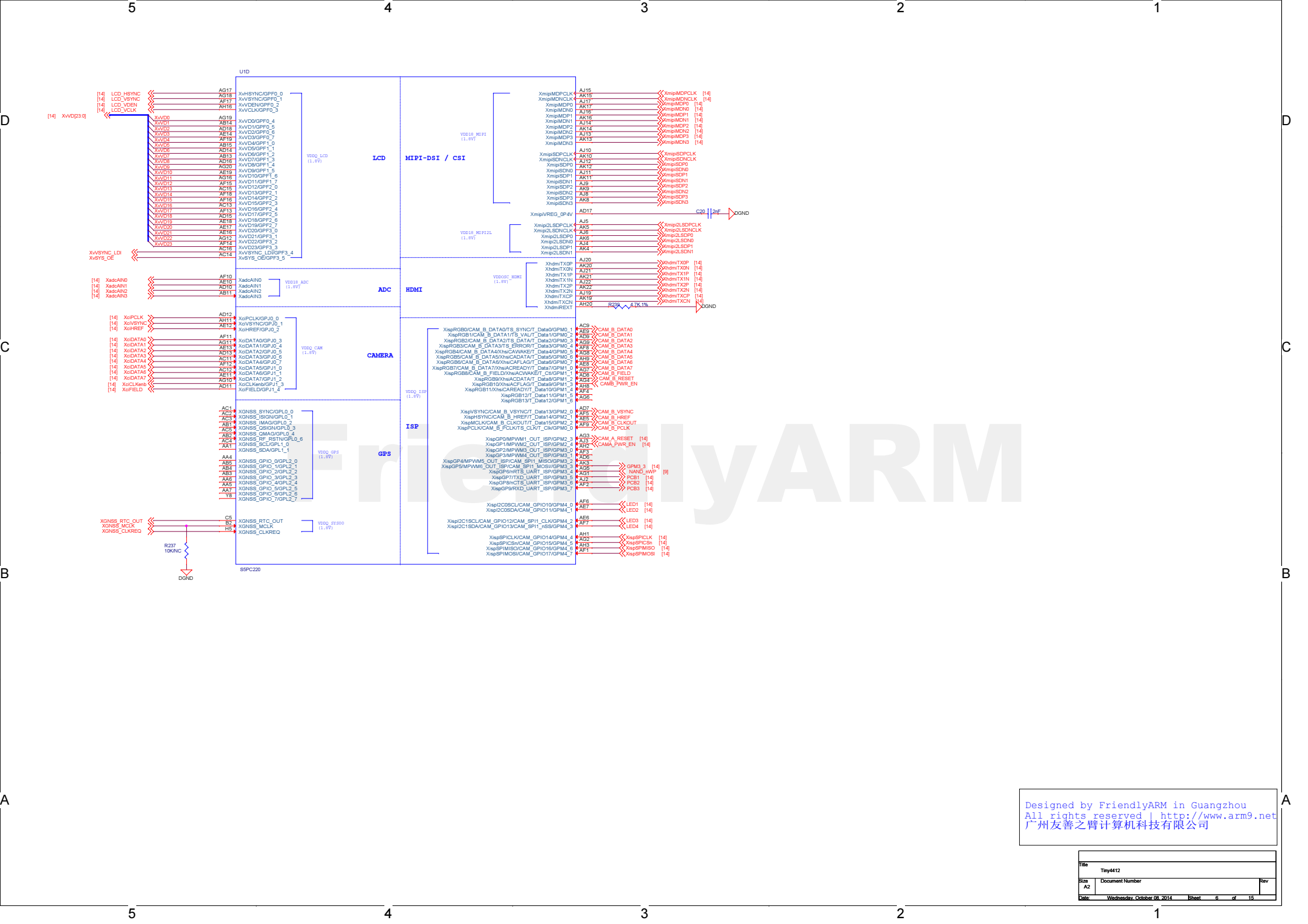
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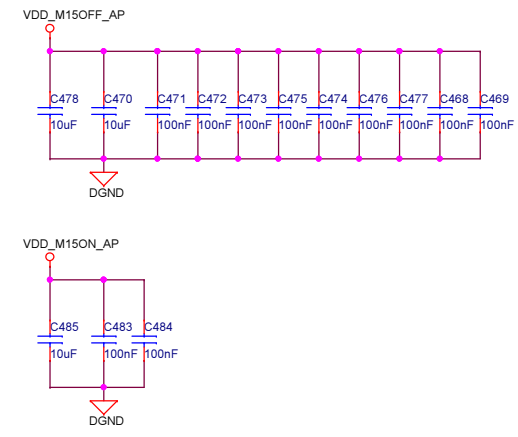
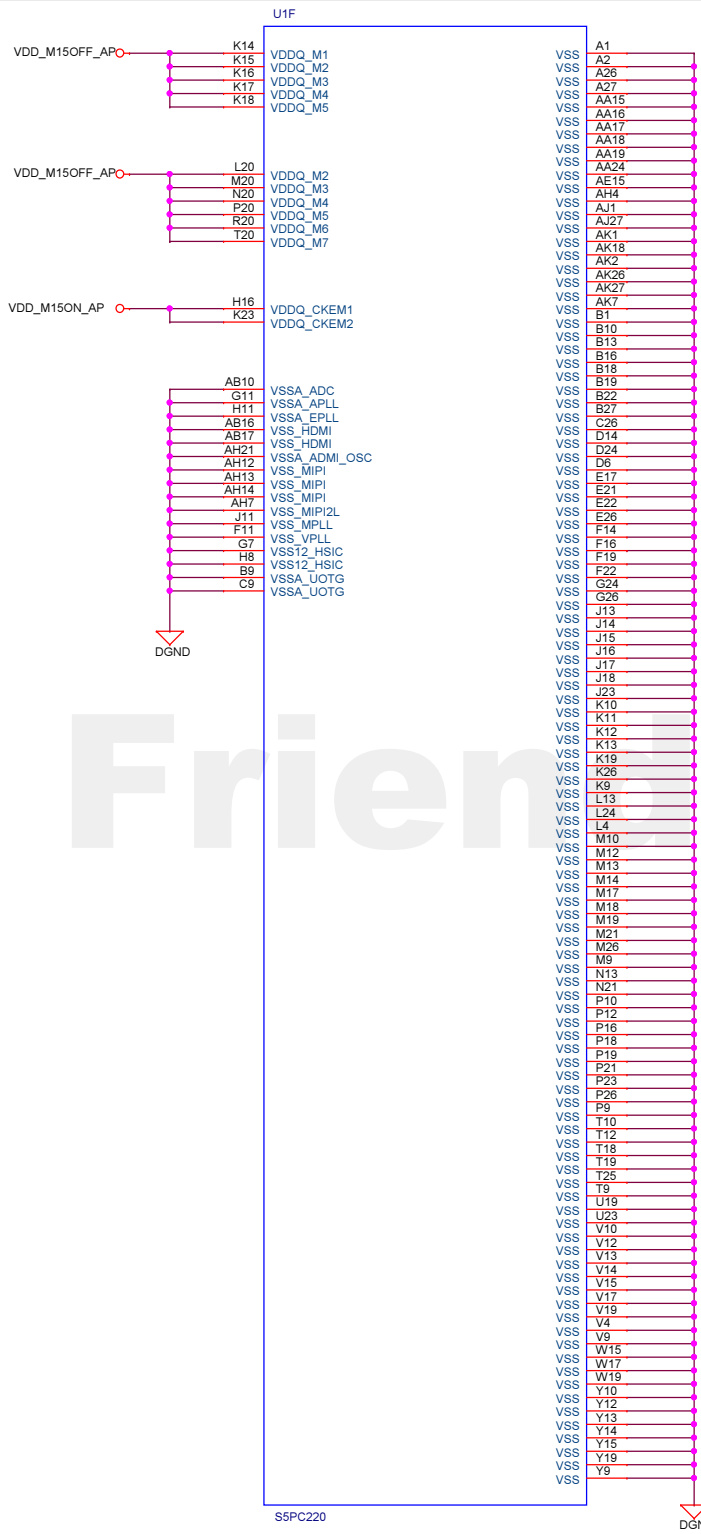
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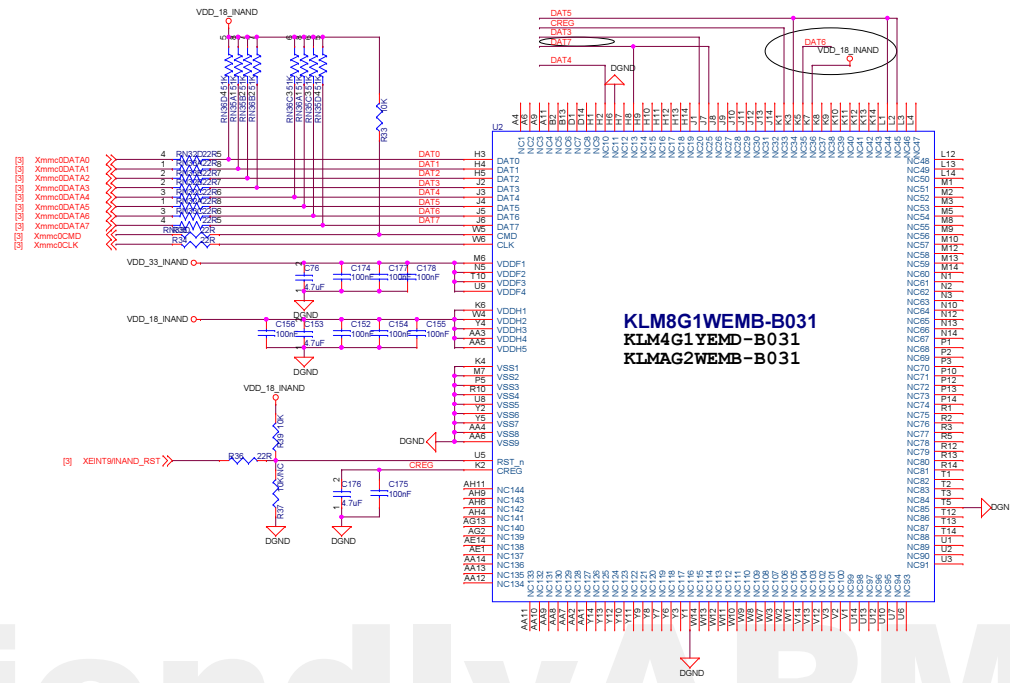
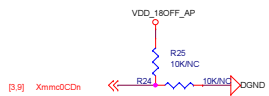
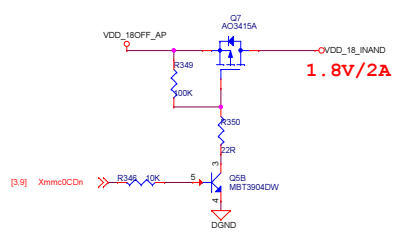
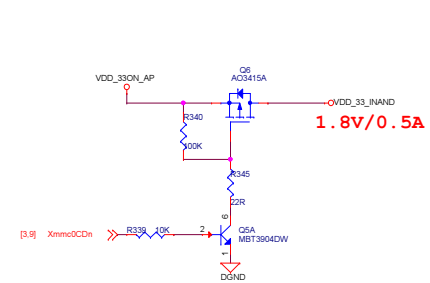
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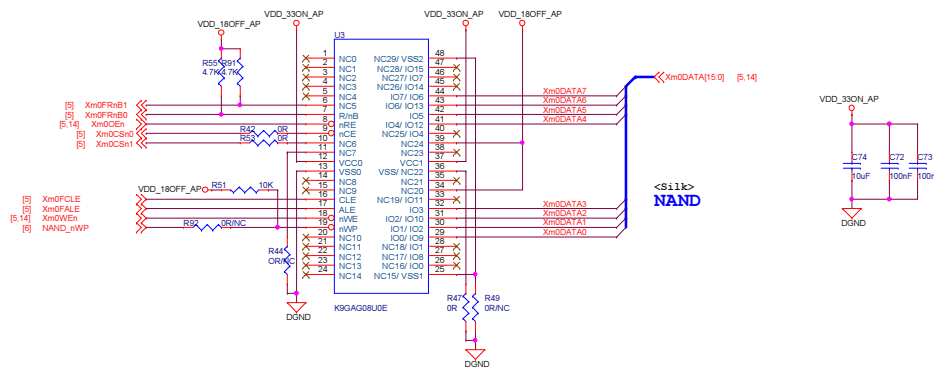
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NAND Flash memory



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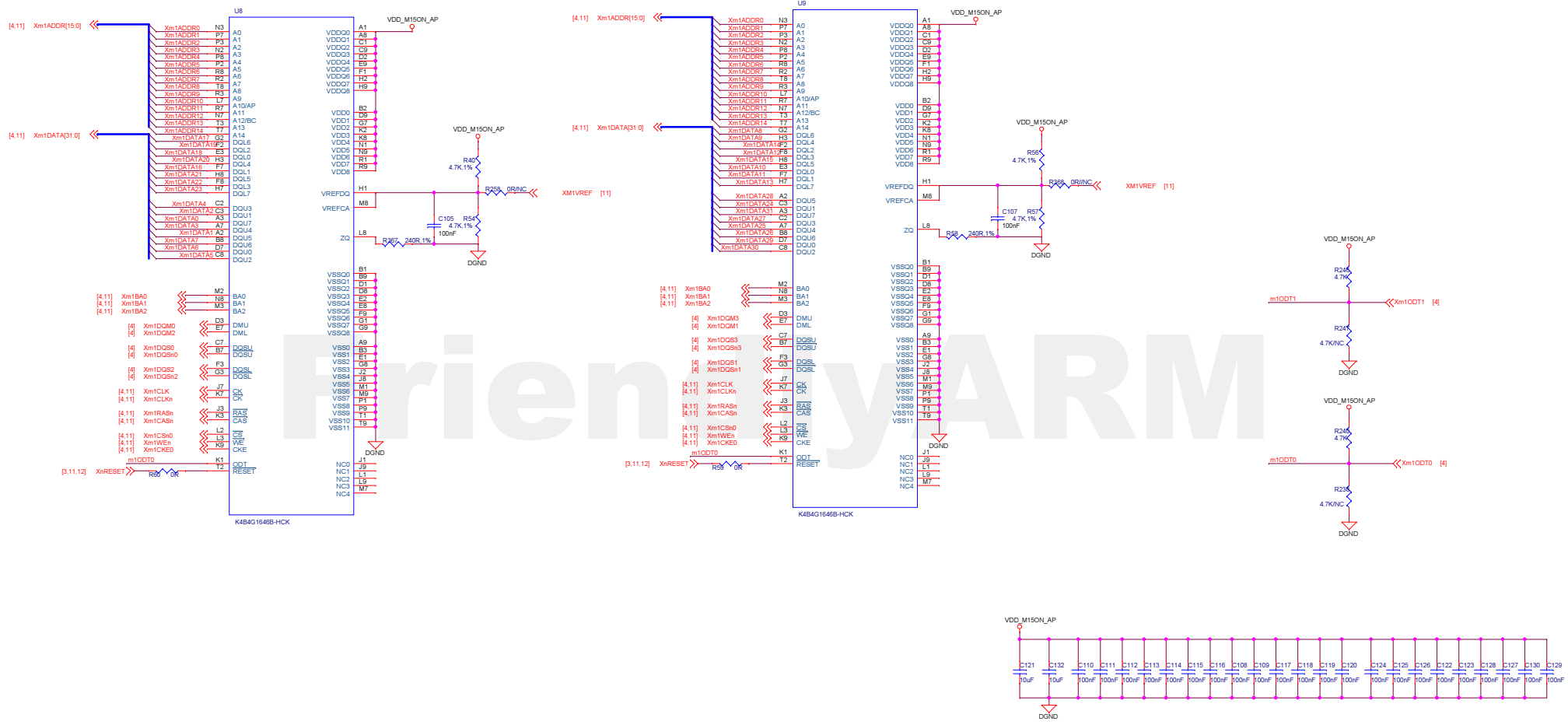
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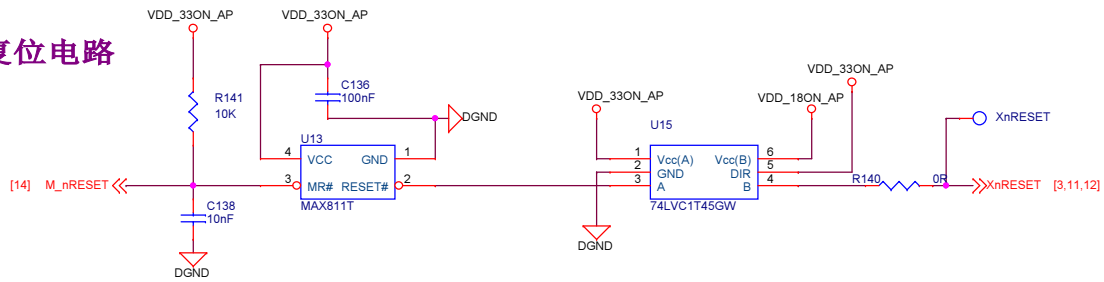
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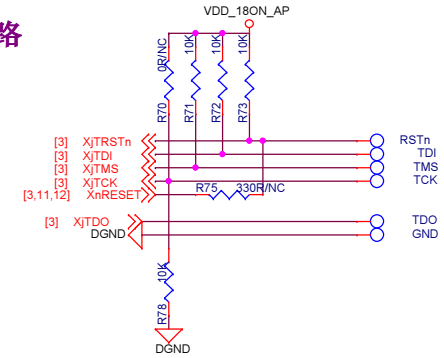


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复位电路

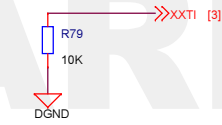


JTAG电路

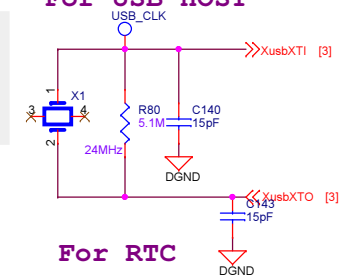


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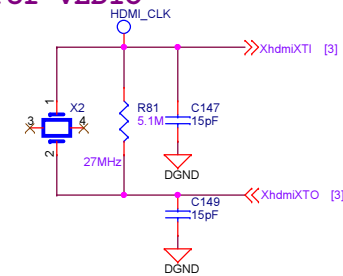
For PLL



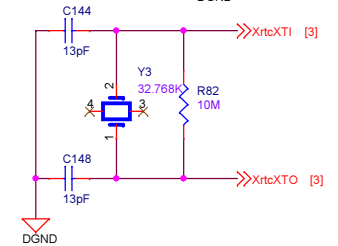
For USB HOST



For VEDIO

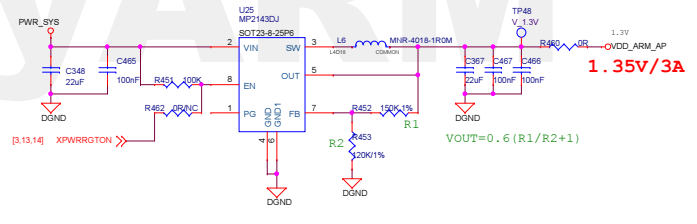
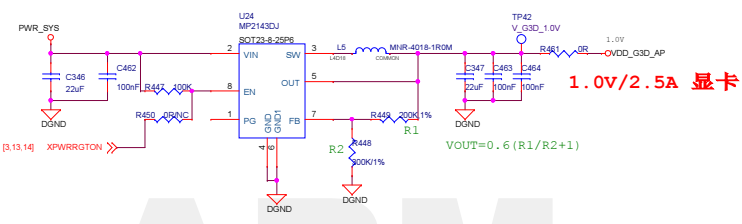
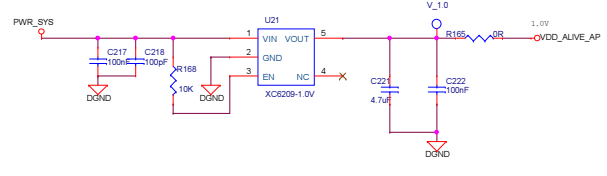
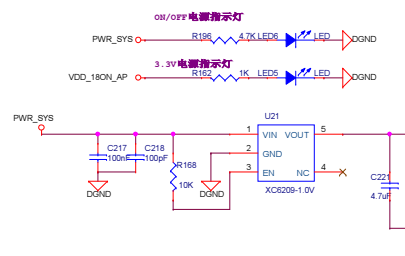
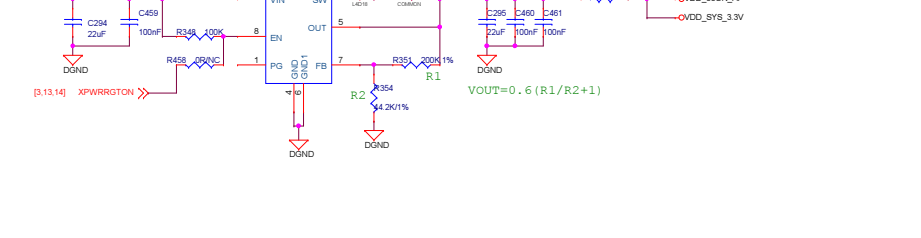
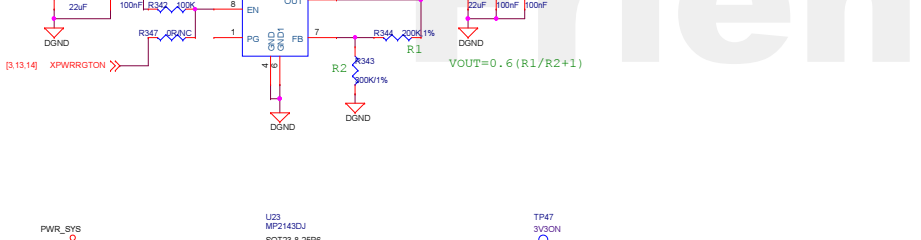
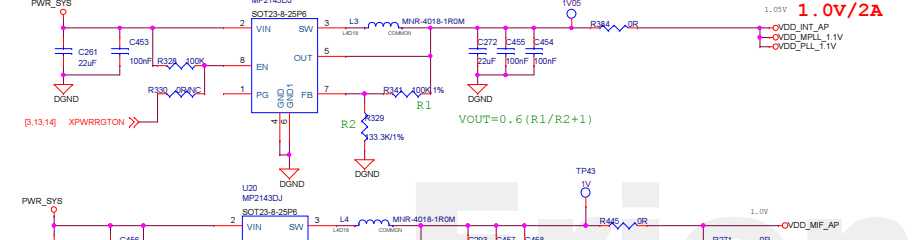
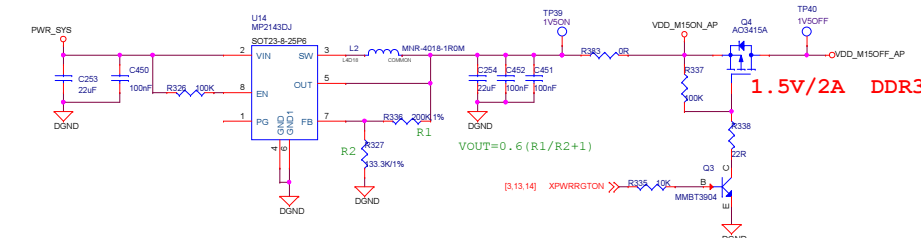
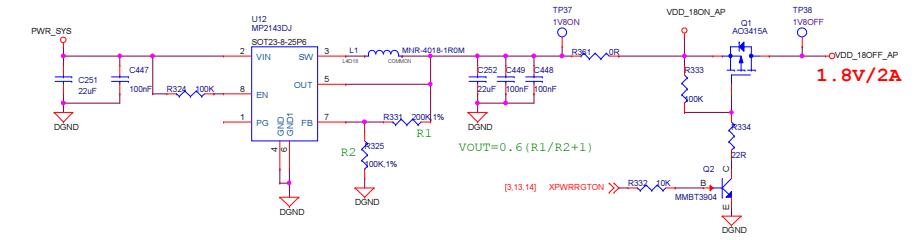


For RTC



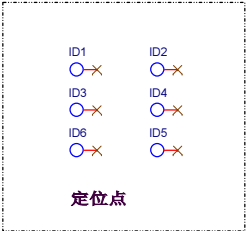
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